

**Fig. 1**  
(Prior Art)

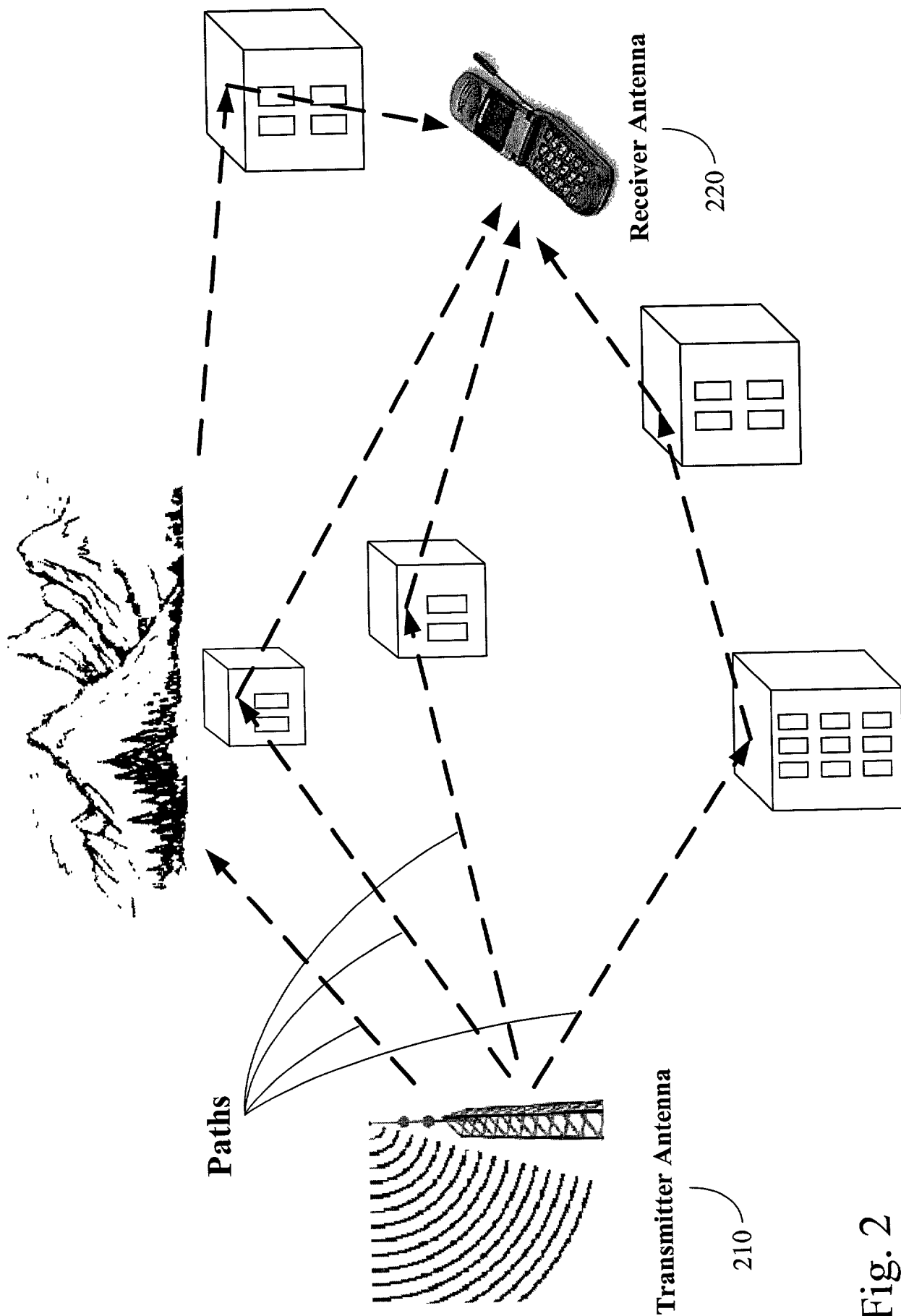


Fig. 2  
(Prior Art)

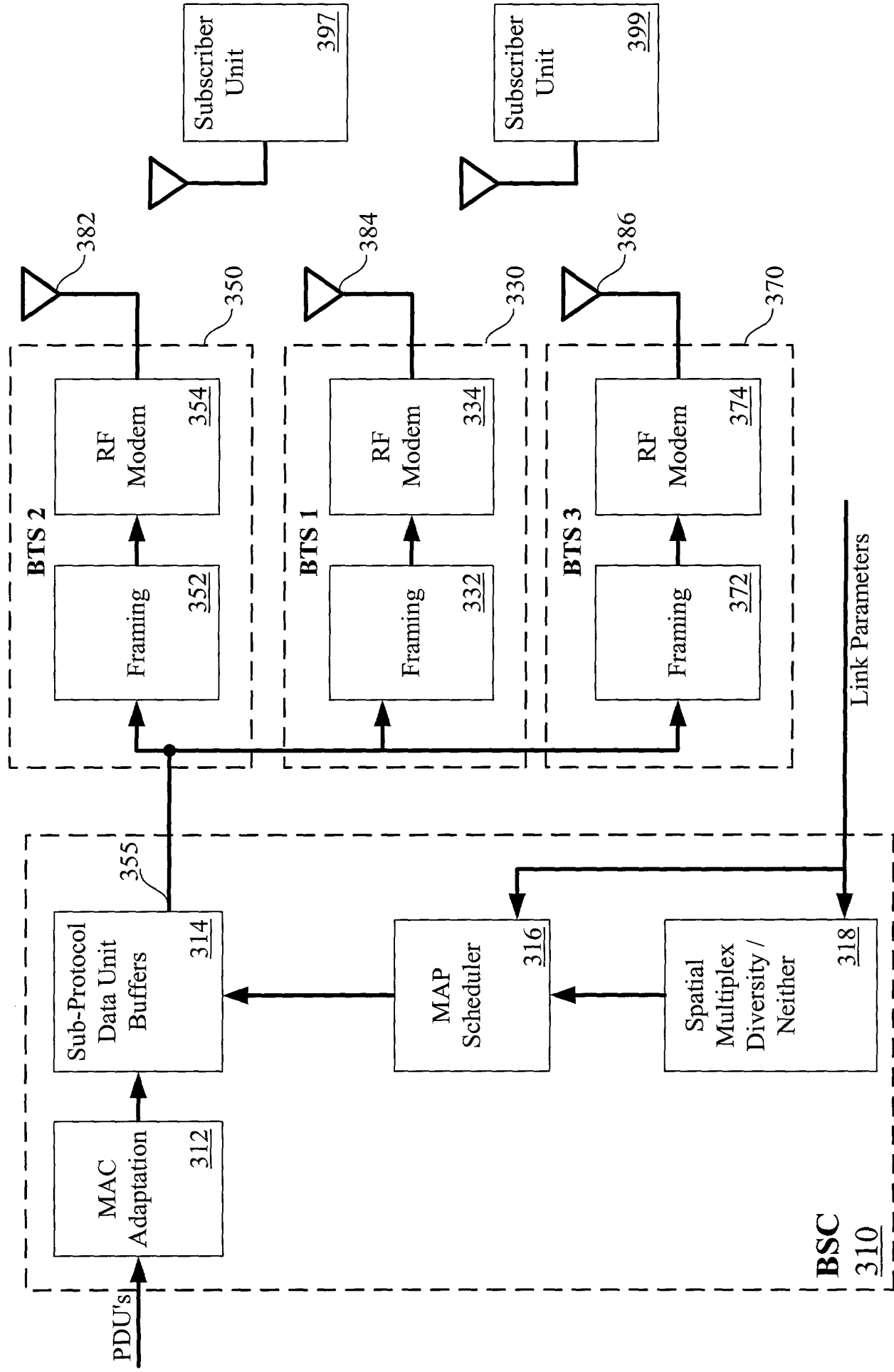


Fig. 3

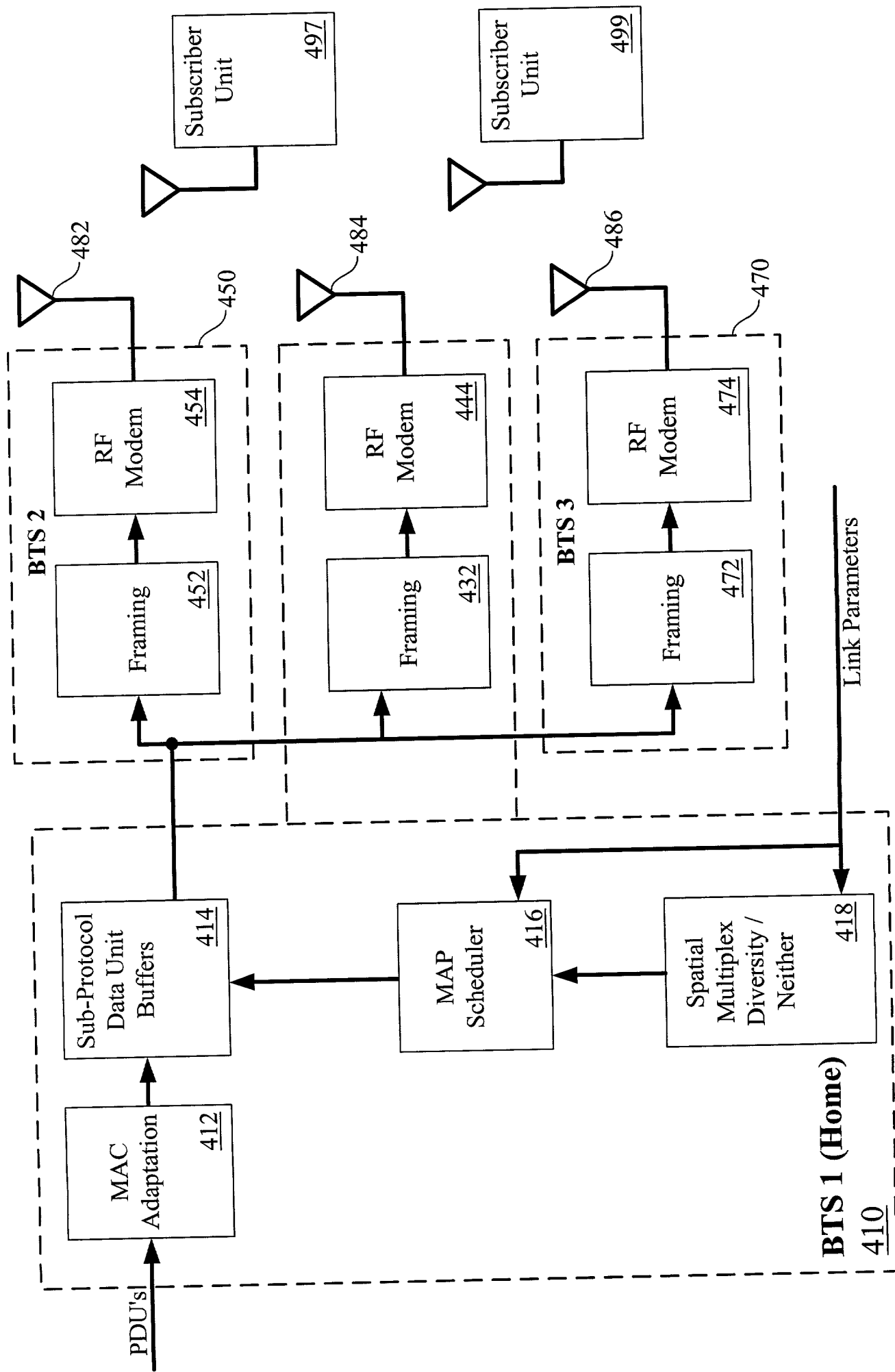


Fig. 4

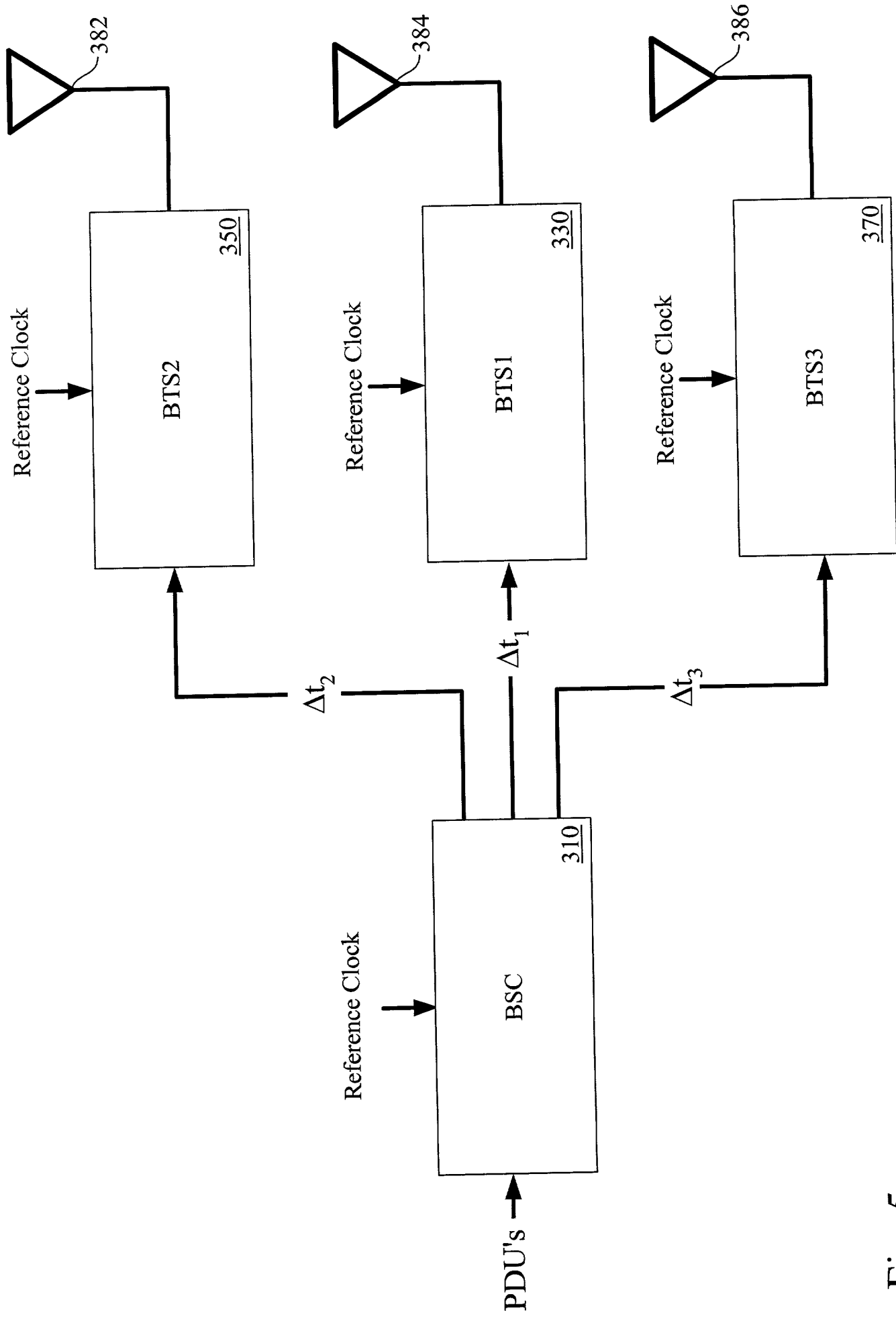


Fig. 5

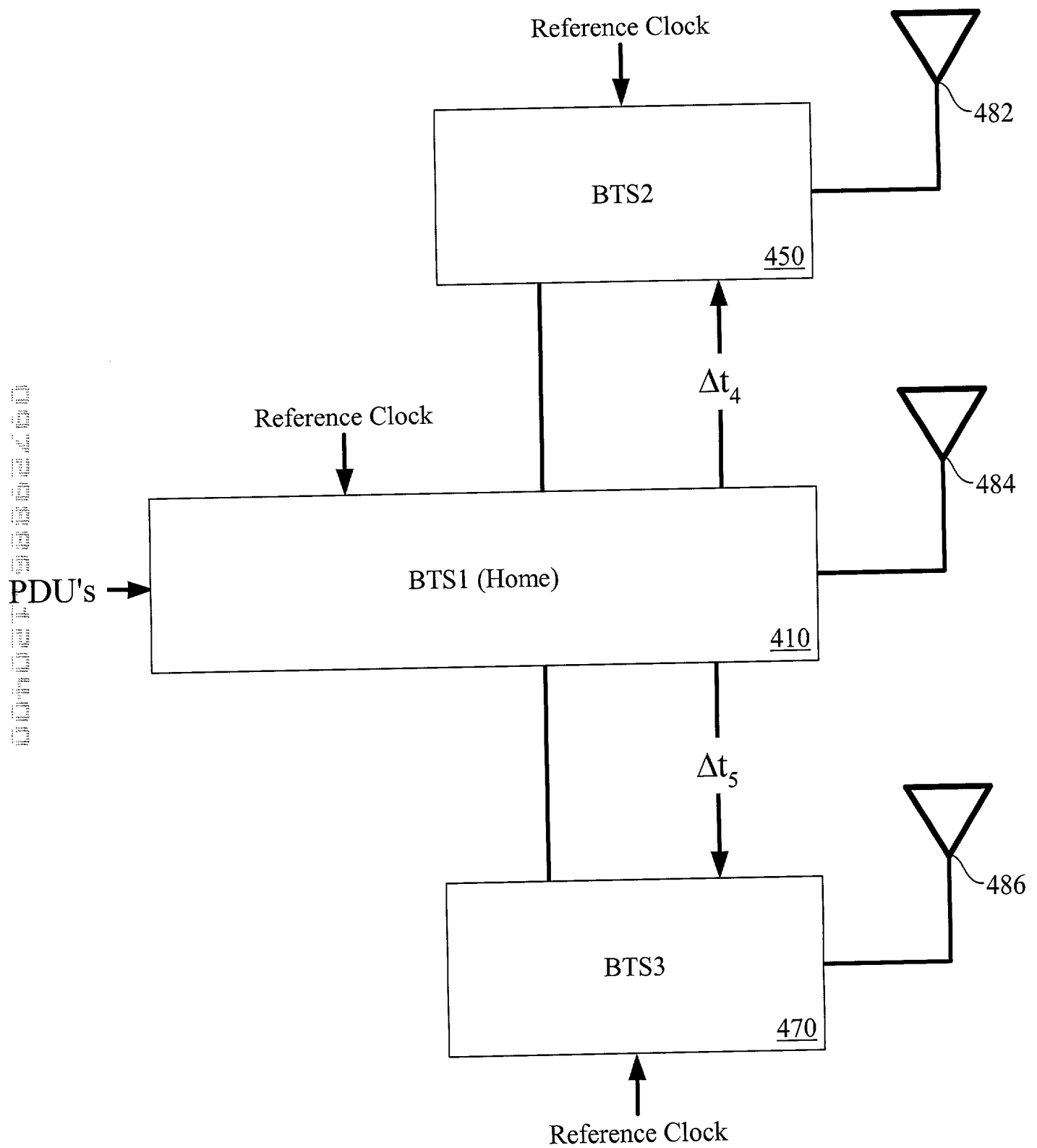


Fig. 6

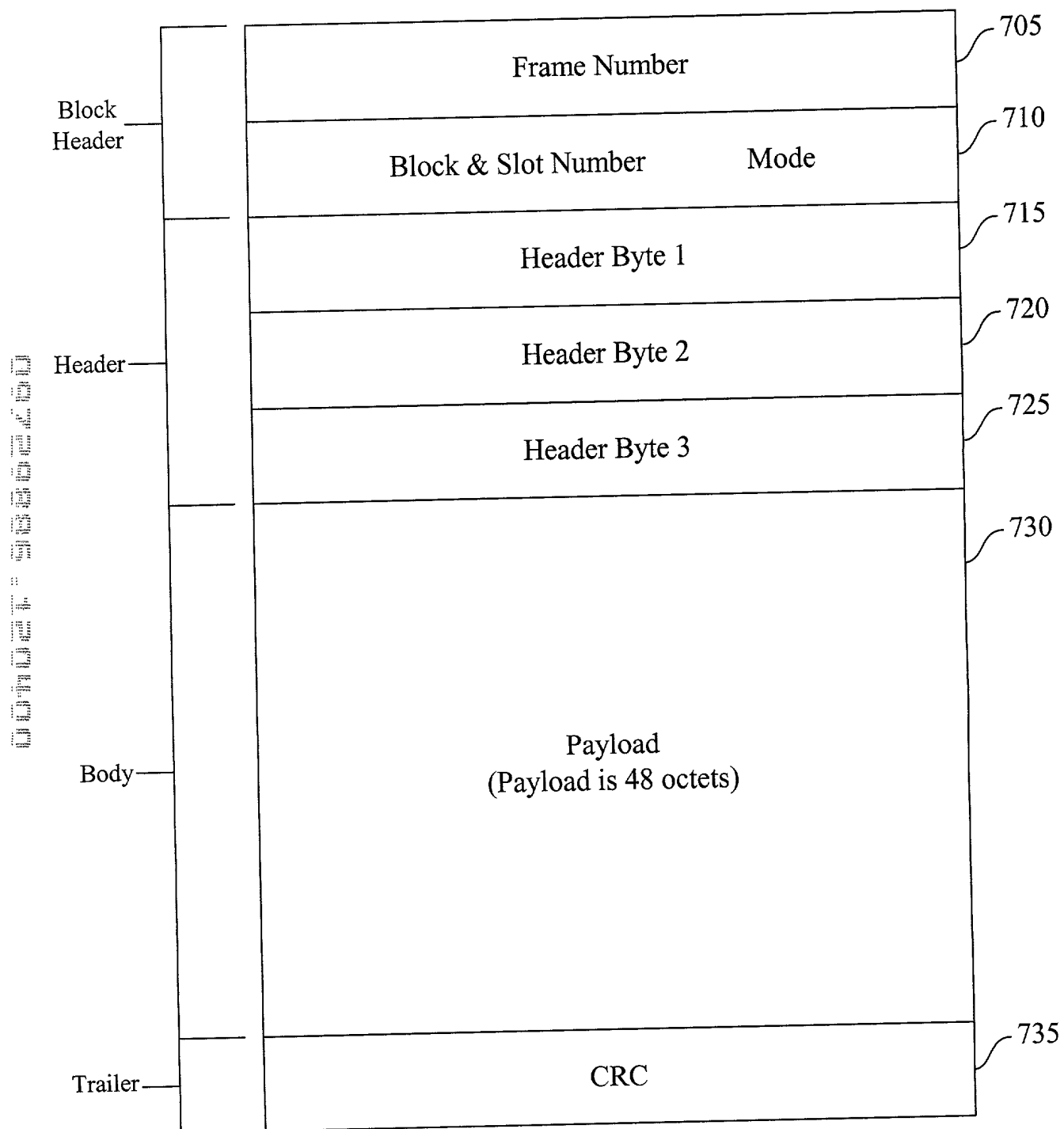


Fig. 7

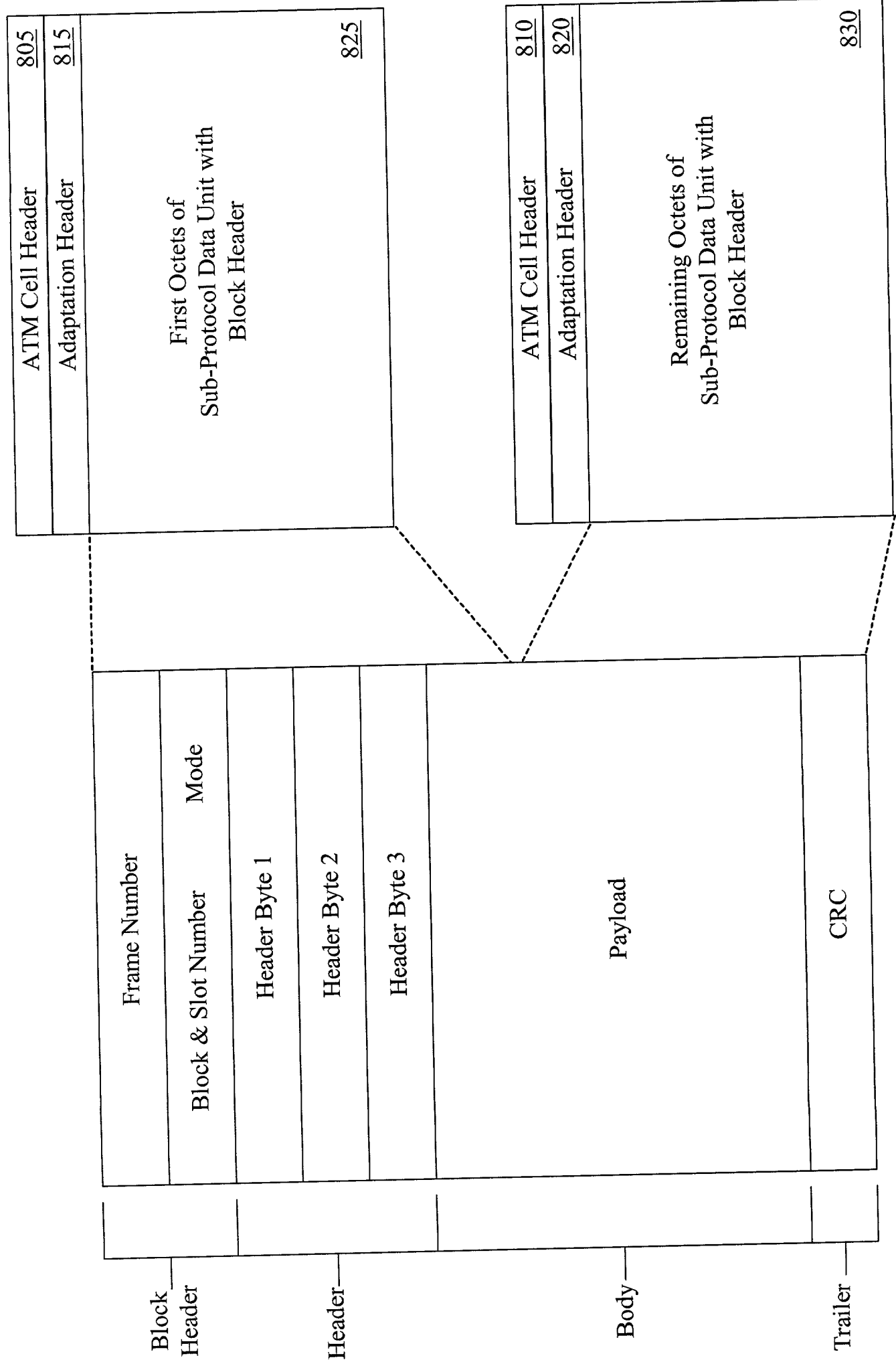


Fig. 8



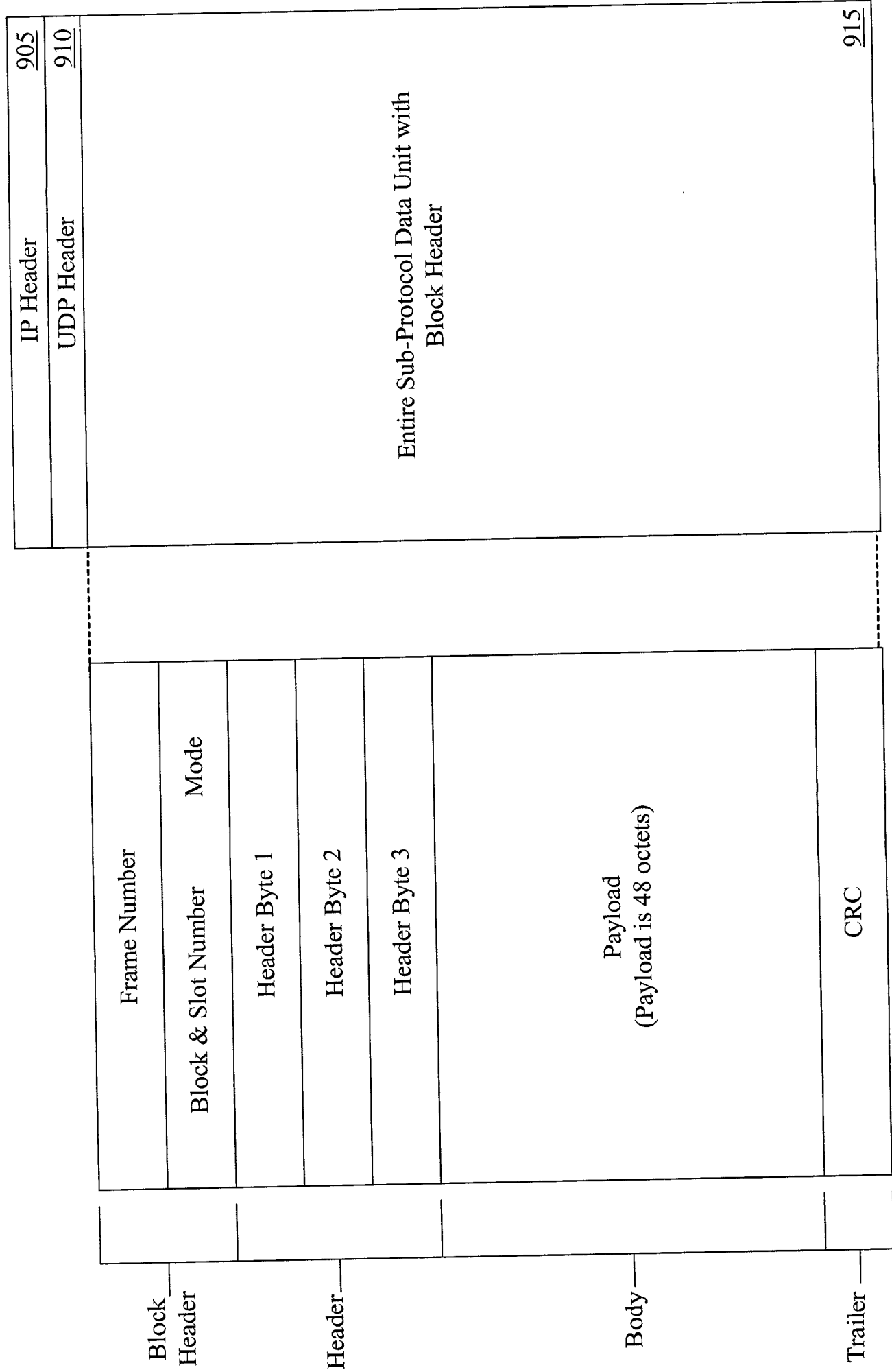


Fig. 9

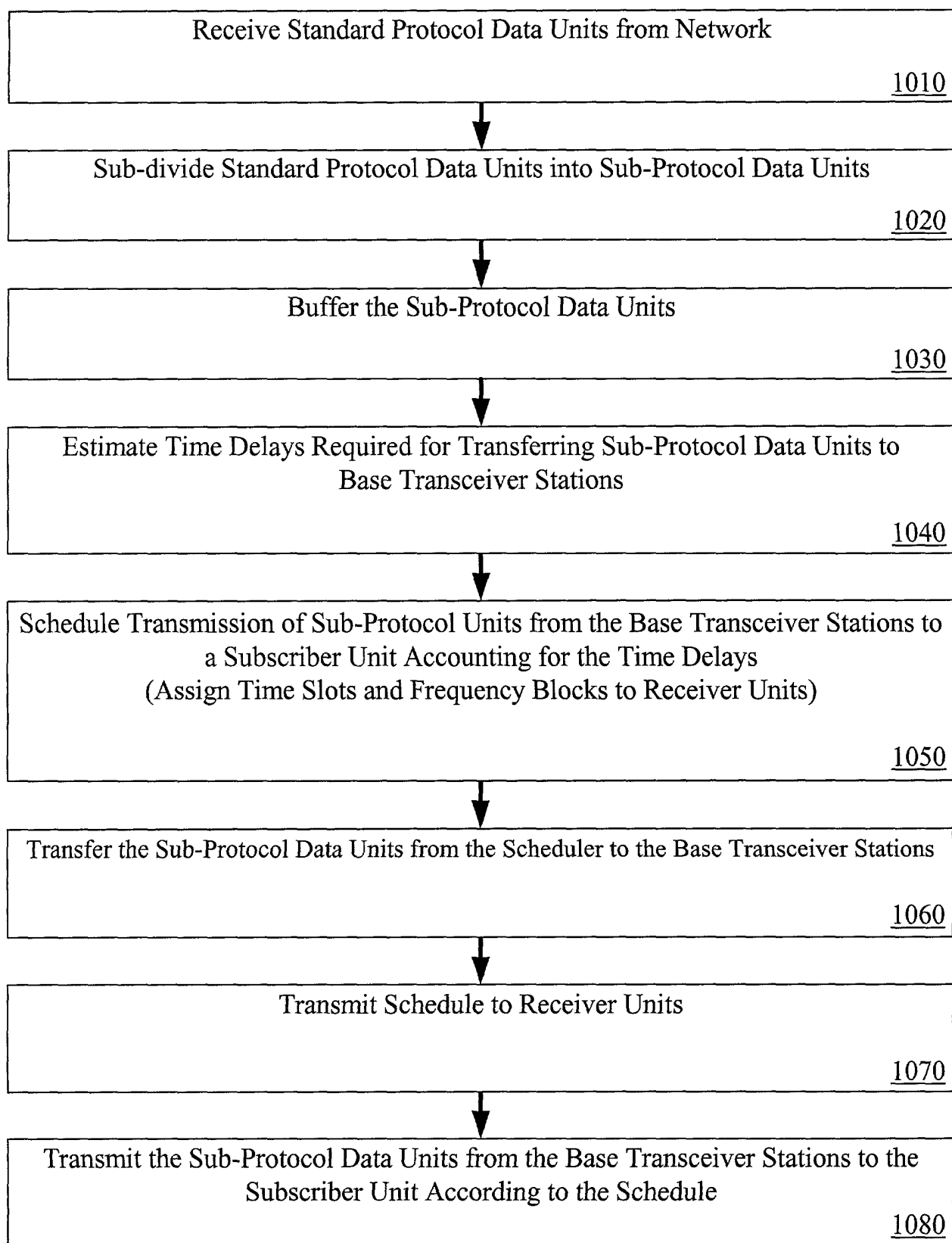


Fig. 10A

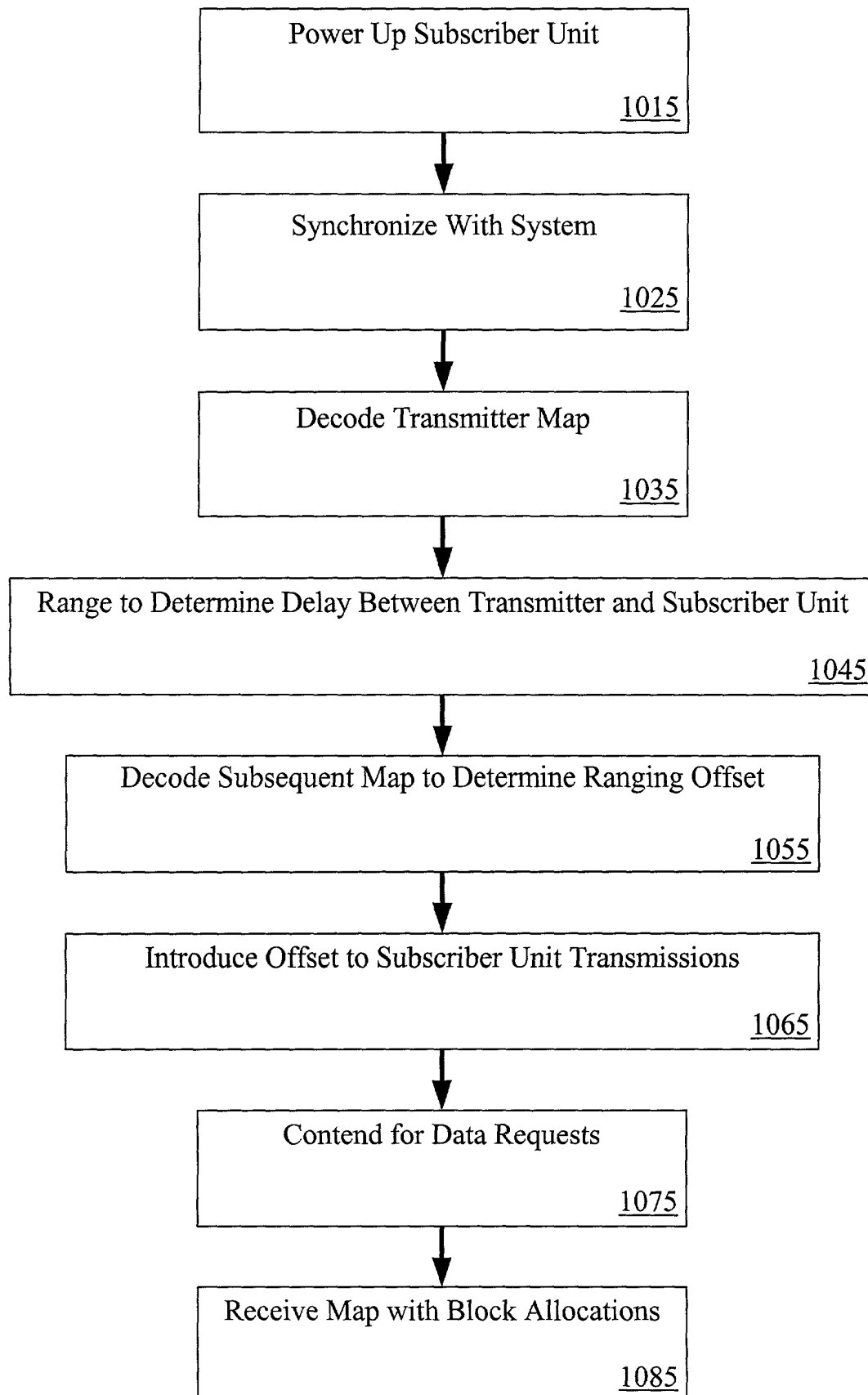


Fig. 10B

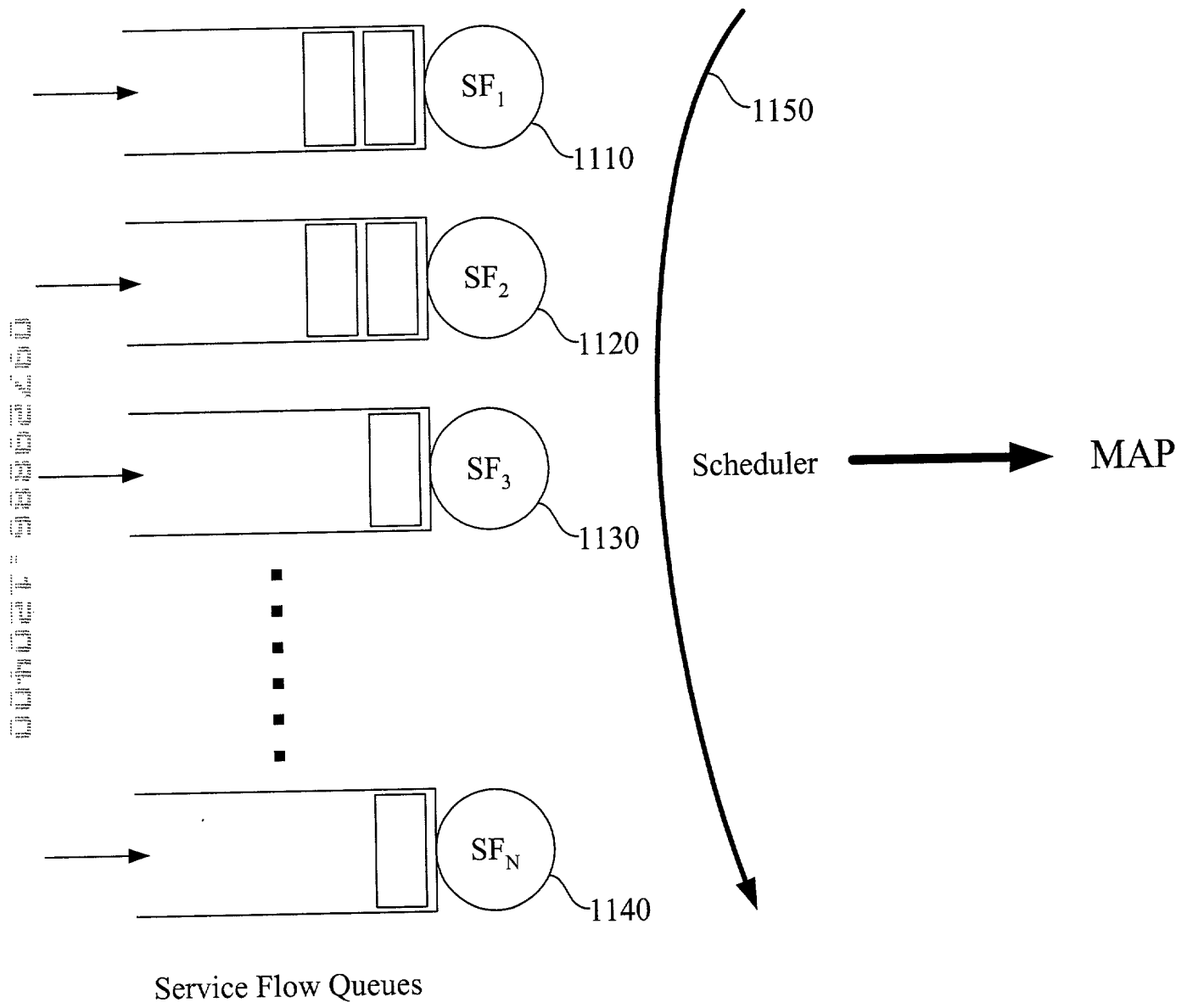


Fig. 11A

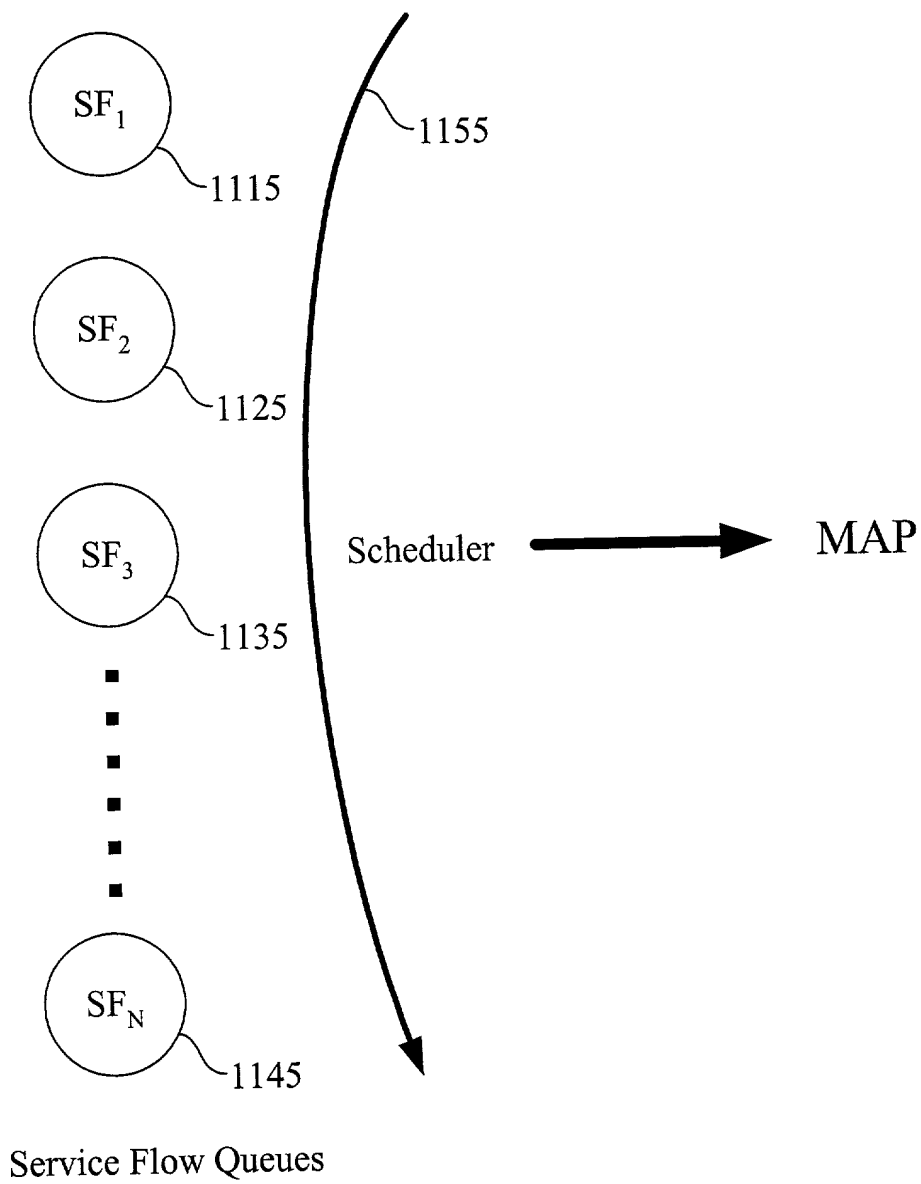


Fig. 11B

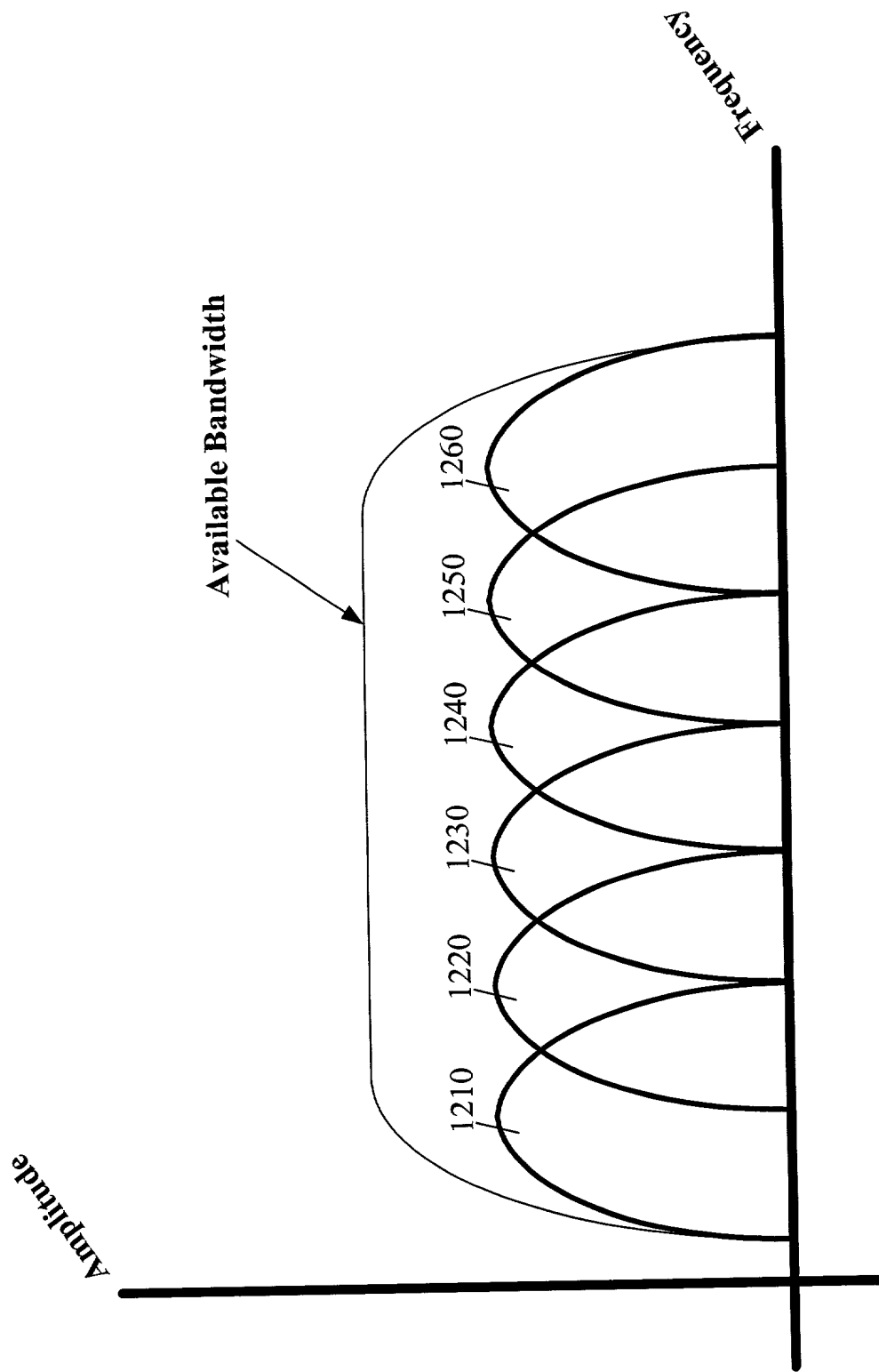


Fig. 12

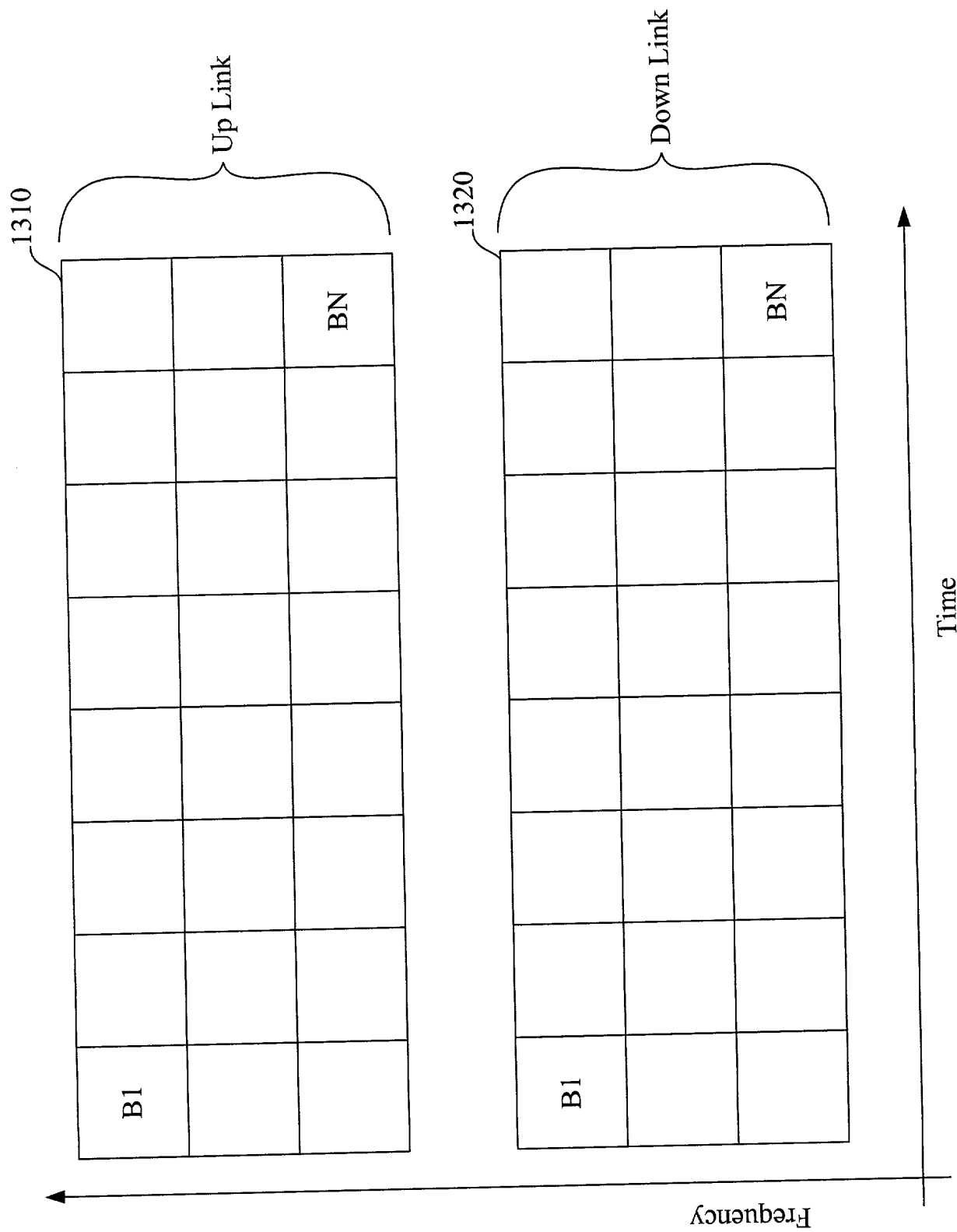


Fig. 13B

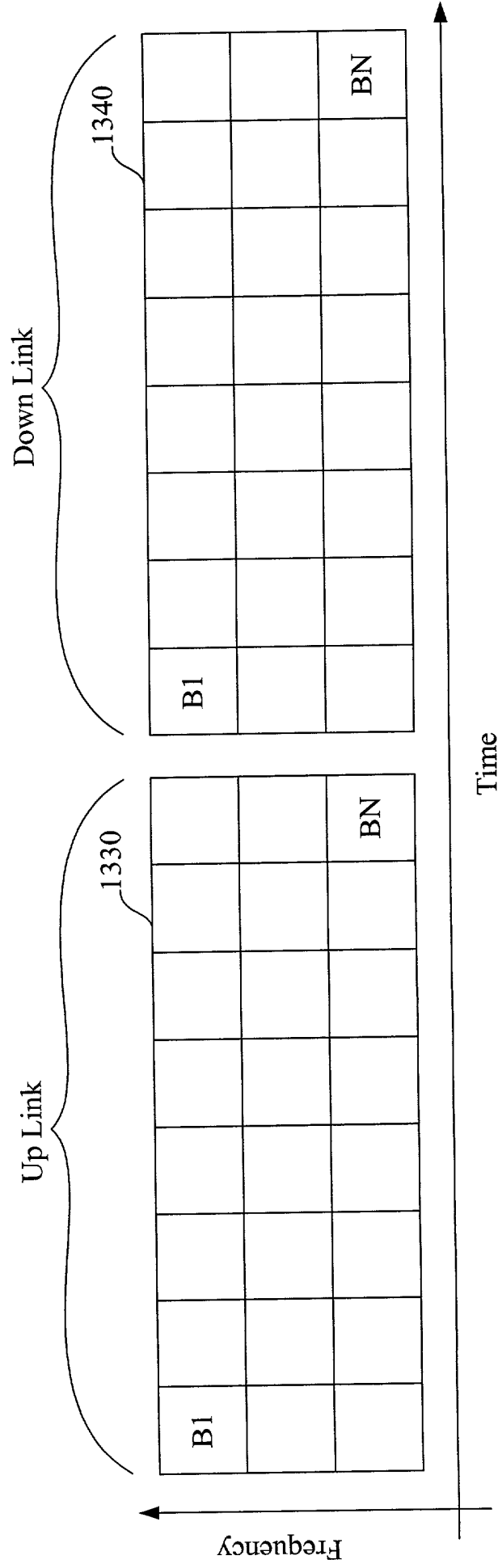


Fig. 13C



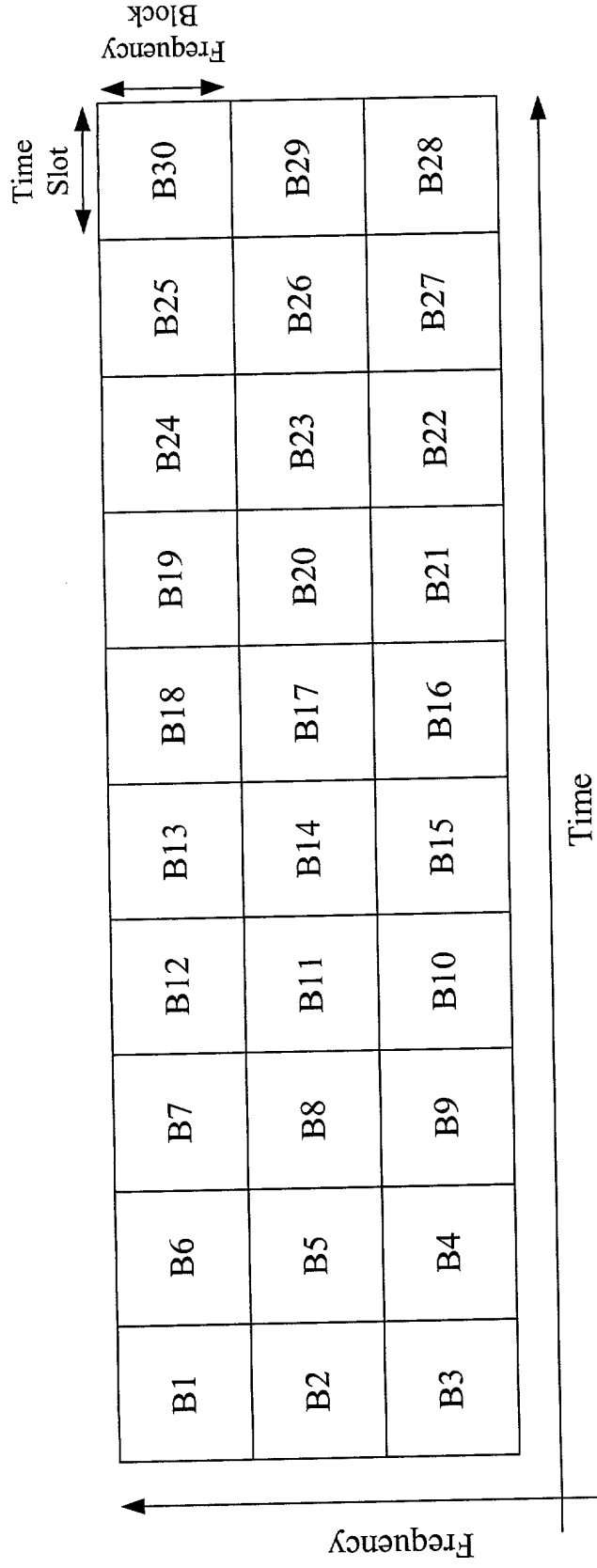


Fig. 13A

SF ID	SF Queue Size	Mode Assignment	Block Weight	System Mode
SF <sub>1</sub>	SFQ <sub>1</sub>	M <sub>1</sub>	BW <sub>1</sub>	SM
SF <sub>2</sub>	SFQ <sub>2</sub>	M <sub>2</sub>	BW <sub>2</sub>	DIV
SF <sub>3</sub>	SFQ <sub>3</sub>	M <sub>3</sub>	BW <sub>3</sub>	Nothing
SF <sub>N</sub>	SFQ <sub>N</sub>	M <sub>N</sub>	BW <sub>N</sub>	DIV

Fig. 14

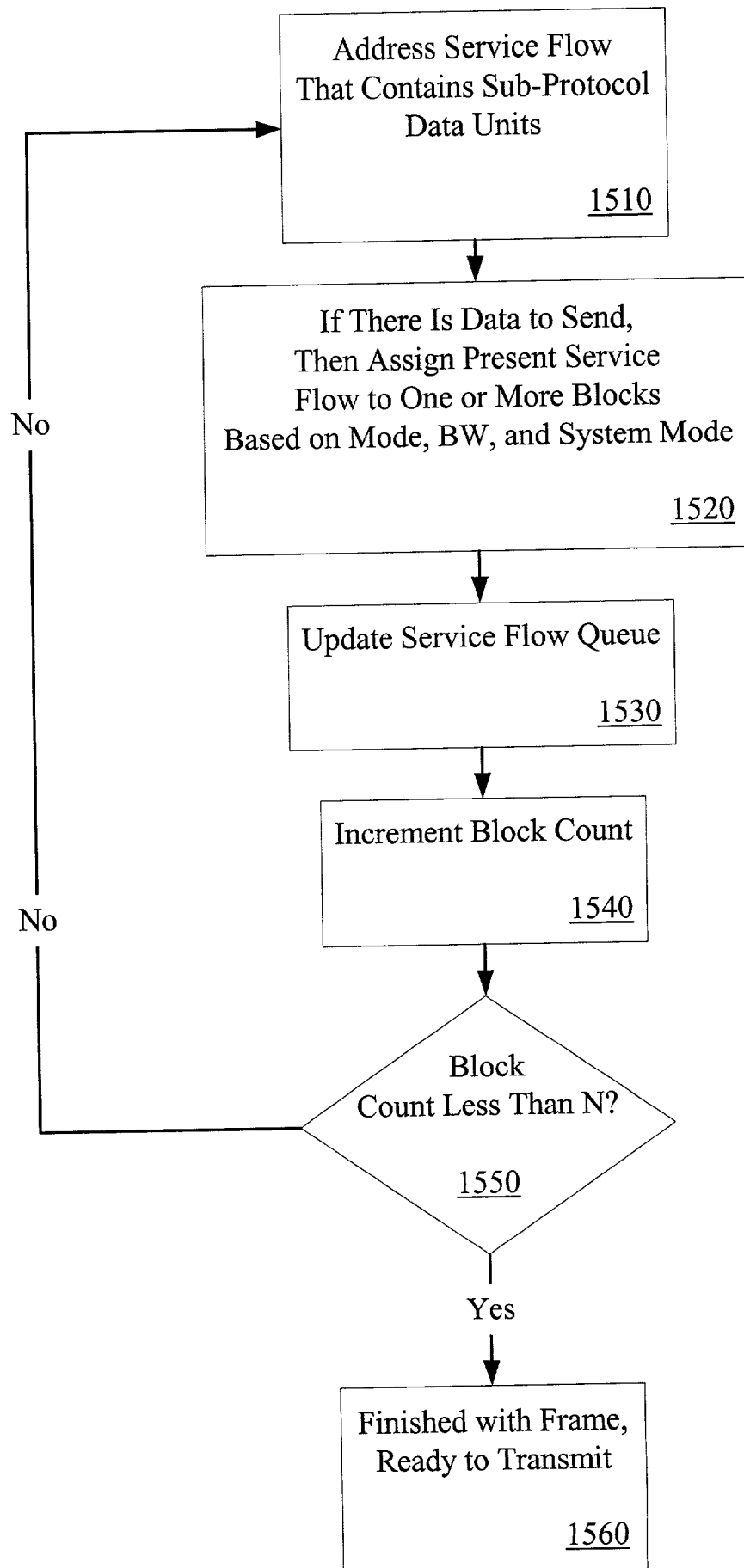


Fig. 15

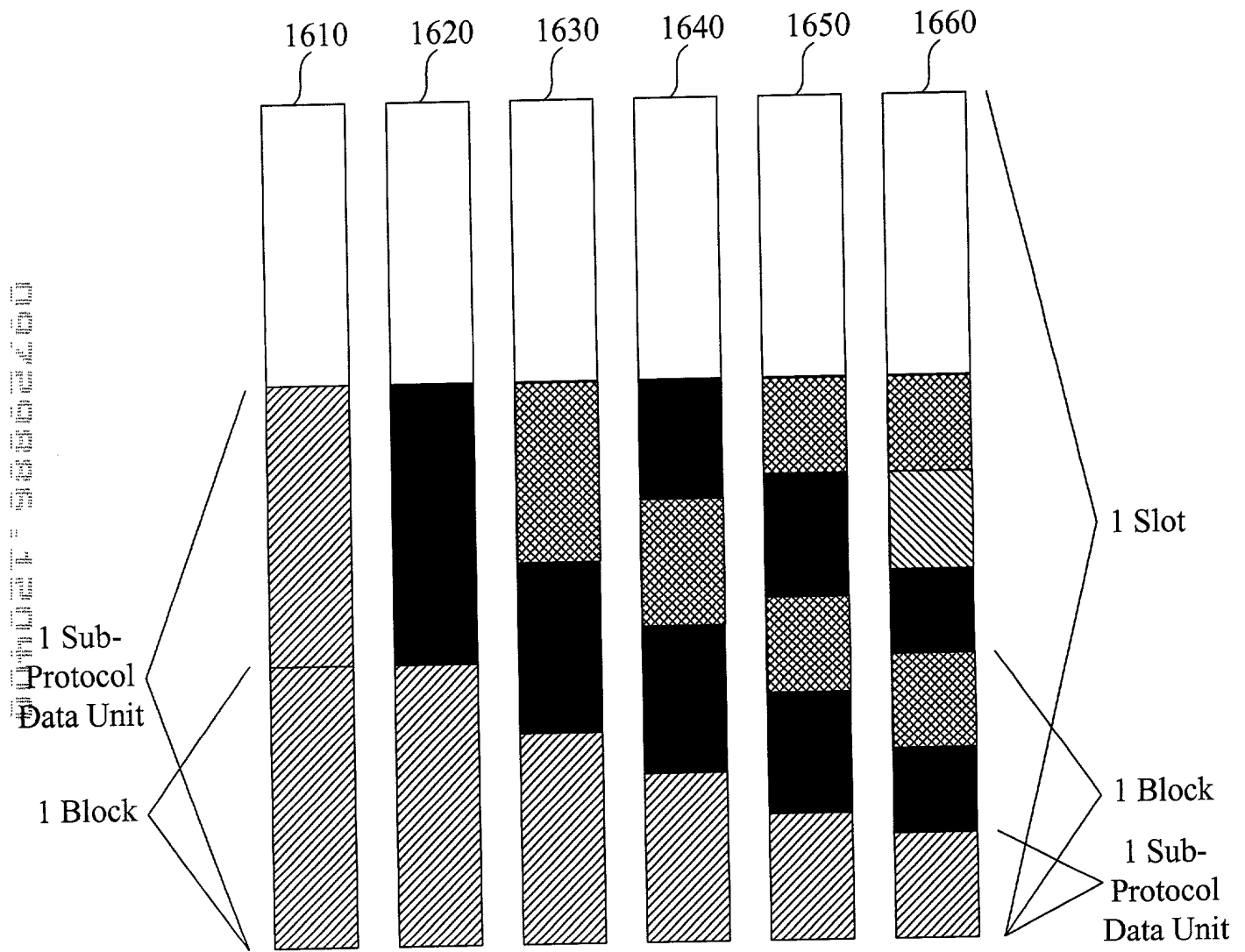


Fig. 16

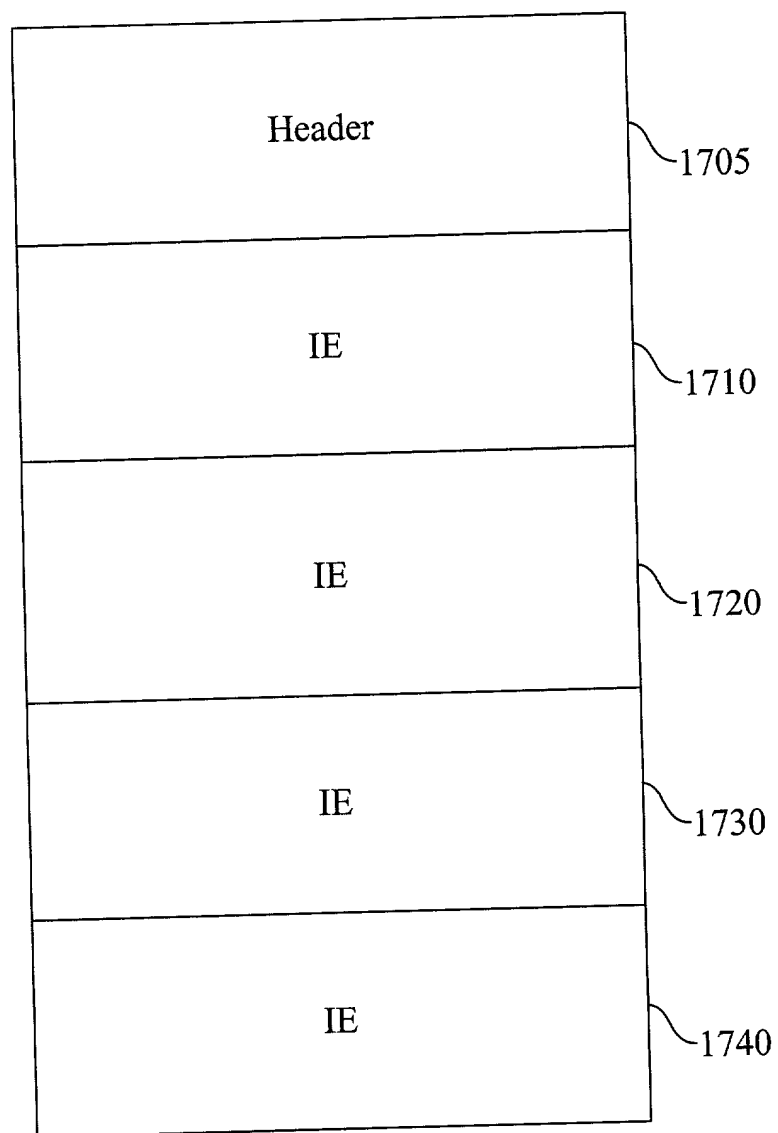


Fig. 17